## Amendments to the Specification

Please **replace** the paragraph beginning at page 5, line 37 with the following **amended** paragraph:

In case a component video signal is applied to the video input 4 and selected by the input switching matrix 6, at the output lines 13, 14, 15 the video signals Pb, Pr and Y are present. In case of a S-VHS signal, a chrominance signal Chroma is present at line 14 and a luminance signal Luma at line 15, and in case of a CVBS signal, the CVBS signal is present at line 15. The line 15 is coupled to a synchronization ~~decoder~~ detector 7, to extract the synchron1zation pulses from the luminance signal, and to a color decoder 8, for controlling the color decoding of a chrominance signal present at line 14. The line 15 is also coupled to the color decoder 8. Further, the lines 13, 14, 15 are coupled to an output switching matrix 9, which, in case a component video signal YPbPr is present, converts the Pb, Pr signals into U and v color signals, for providing YUV signals at ~~it is~~ its output, ready for a processing in a display processing unit 11. The display processing of the video signals within the television set 2 is not further explained here, because it is well known in prior art. It provides respective output signals for a display 12, for example a cathode ray tube (CRT), a LCD or a plasma panel. Also not shown here is a tuner section of the television set 2, which is necessary for viewing terrestrial or cable television programs and which is usually also coupled to the input switching matrix 6.

Please **replace** the paragraph beginning at page 6, line 25 with the following **amended** paragraph:

In case a CVBS signal is present at the output line 15, the synchronization signal ~~decoder~~ detector 7 and the color decoder 8 provide respective output signals YUV at the output of the color decoder 8. This is also the case when an S-VHS signal is present at lines 14 and 15. The output of the color decoder 8 is also coupled to the output switching matrix

9. The input switching matrix 6, the synchronization signal detector 7 and the color decoder 8 are each coupled to and controlled by a microcontroller 10, for example via an I2C bus.

Please **replace** the paragraph beginning at page 6, line 36 with the following **amended** paragraph:

According to the invention, the recognition of the respective signals YPrPb, Chroma/Luma in case of an S-VHS signal and CVBS at output lines 13, 14, 15 is made automatic by means of the synchronization signal detector 7, the color decoder and the microcontroller 10. When a user switches the input switching matrix 6 for example to the video input 4, for viewing a video provided by a DVD player being coupled to the video input 4, then at the output lines 13, 14 and 15 a video signal is present. The input switching matrix 6 is set in response to the microcontroller [[7]] 10, as instructed by the user. The Y signal present at line 15 is then passed to the synchronization signal [[decoder 4]] detector 7.

Please **replace** the paragraph beginning at page 7, line 11 with the following **amended** paragraph:
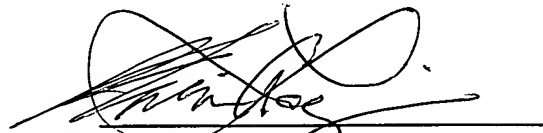
Then the microcontroller waits for a specified period of time, for example a few milliseconds, to allow for a signal to stabilize. Then the microcontroller [[7]] 10 accesses the synchronization signal detector [[4]] 7 to get a feedback on the validity or stability of a luminance signal. If the luminance signal is valid, then a component video input is present at video input 4, and which is then switched through by the output video matrix 9 for displaying. If no valid luminance signal is present at the synchronization detector 7, then the microcontroller 10 switches the input switching matrix to the S-VHS terminals S-Video of the video input 4. Then the S-VHS input terminal is tested for a valid synchronization signal. In case no S-VHS signal is present, then the input switching matrix 6 switches to the video terminal of the video input 4, for checking for a valid CVBS signal.

Please **replace** the paragraph beginning at page 7, line 28 with the following **amended** paragraph:

Component video signals and S-VHS can be distinguished in particular by means of the color decoder 8. For example, the color decoder 8 can check for a color burst signal on line 14, which is present within the chrominance signal of the SVHS signal, and which is not present in the respective component signal Pr, or Pb. A respective feedback from the color decoder 8 is made to the microcontroller 10 via the control lines. Also a CVBS/S-Video detection already exists in television set, it is according to the present invention also possible to detect and distinguish the component video signals by the microcontroller 10, when using the synchronization signal detector 7, and the color ~~detector~~ <u>decoder</u> 8.

No fee is believed due. However, if a fee is due, please charge the additional fee to Deposit Account 07-0832.

Respectfully submitted,

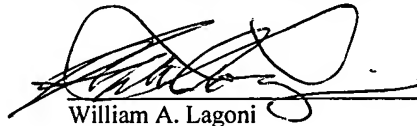By: William A. Lagoni
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November 15, 2006

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## CERTIFICATE OF MAILING

I hereby certify that this amendment is being deposited with the United States Postal Service as First Class Mail, postage prepaid, in an envelope addressed to Mail Stop AMENDMENT, Commissioner for Patents, Alexandria, Virginia 22313-1450 on:

_11/15/06_
Date

William A. Lagoni